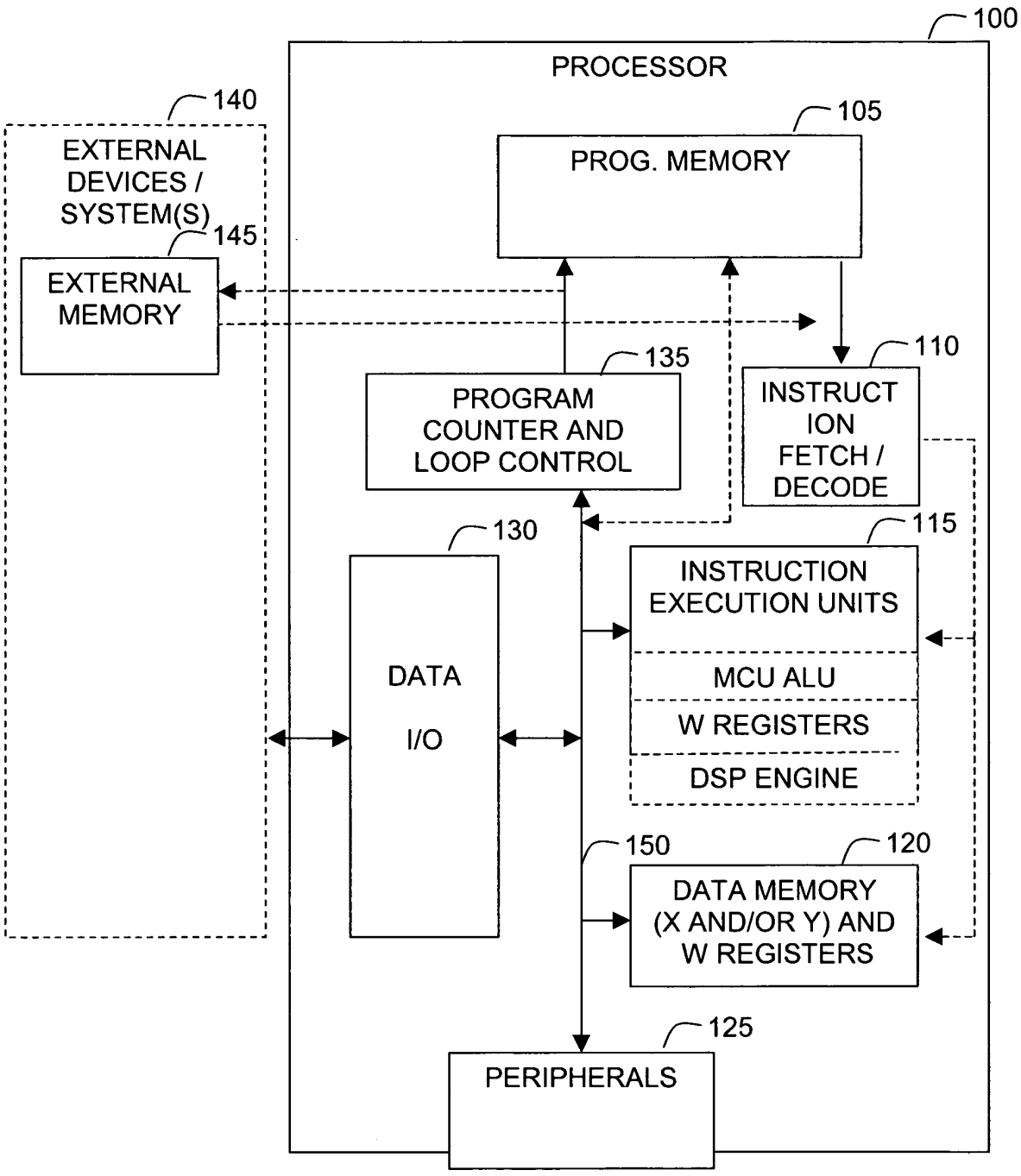
100
PROCESSOR
140
EXTERNAL DEVICES / SYSTEM(S)
145
EXTERNAL MEMORY
105
PROG. MEMORY
135
PROGRAM COUNTER AND LOOP CONTROL
110
INSTRUCT ION FETCH / DECODE
130
DATA I/O
115
INSTRUCTION EXECUTION UNITS
MCU ALU
W REGISTERS
DSP ENGINE
150
120
DATA MEMORY (X AND/OR Y) AND W REGISTERS
125
PERIPHERALS

FIG. 1

FOR OFFICIAL USE ONLY

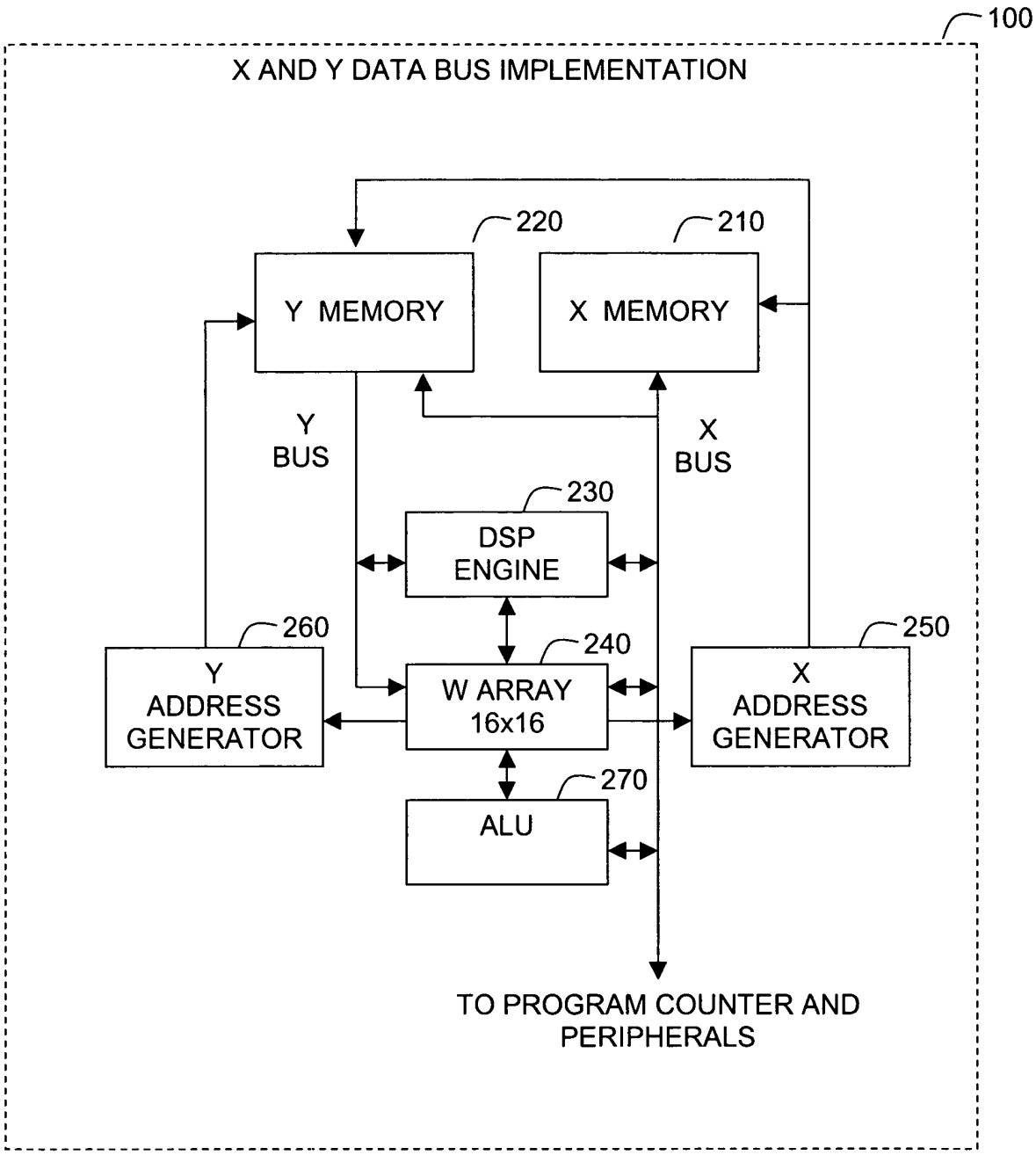

FIG. 2

Patent Application Publication

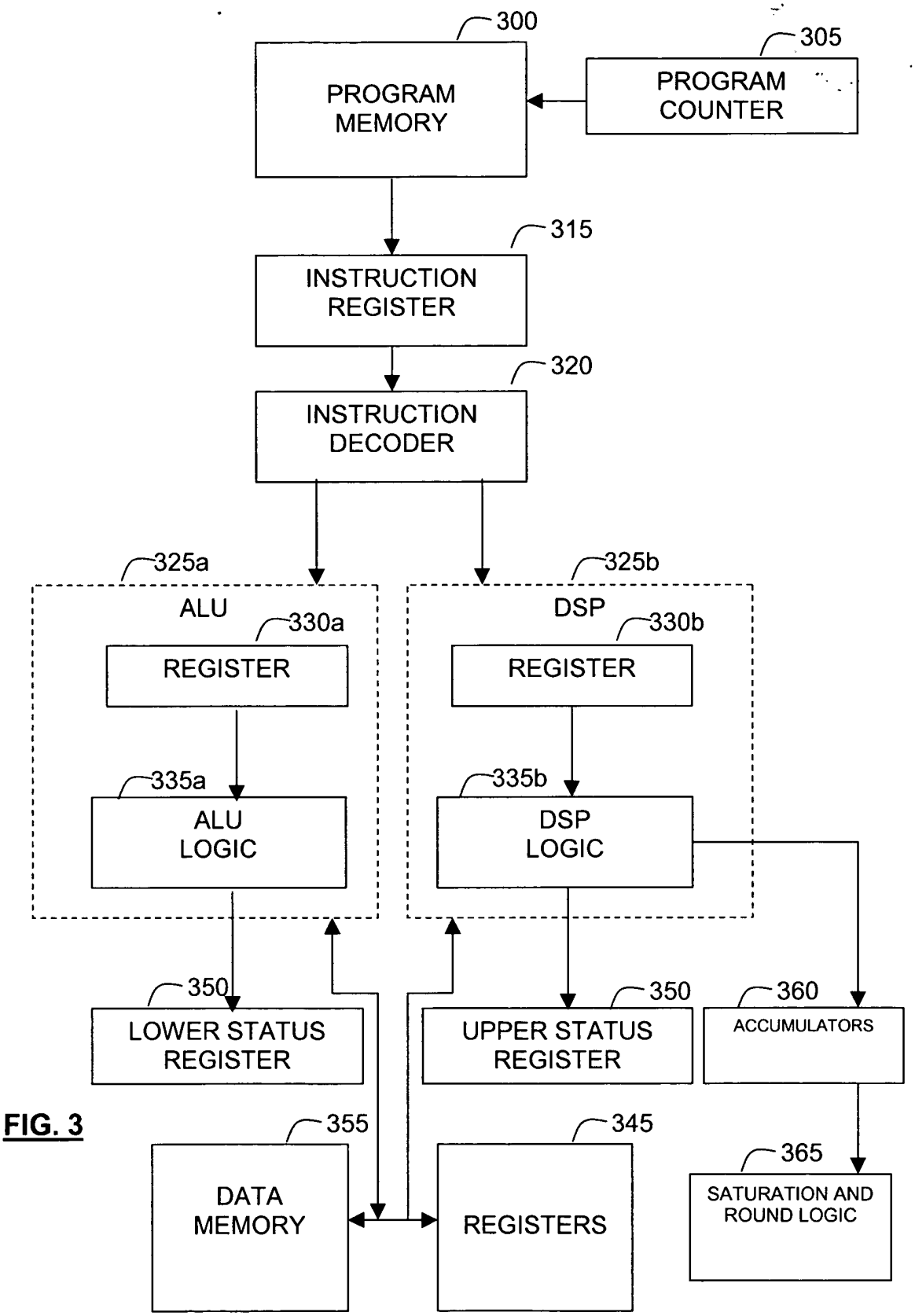

FIG. 3

Express Mail Label No. EV339976960US

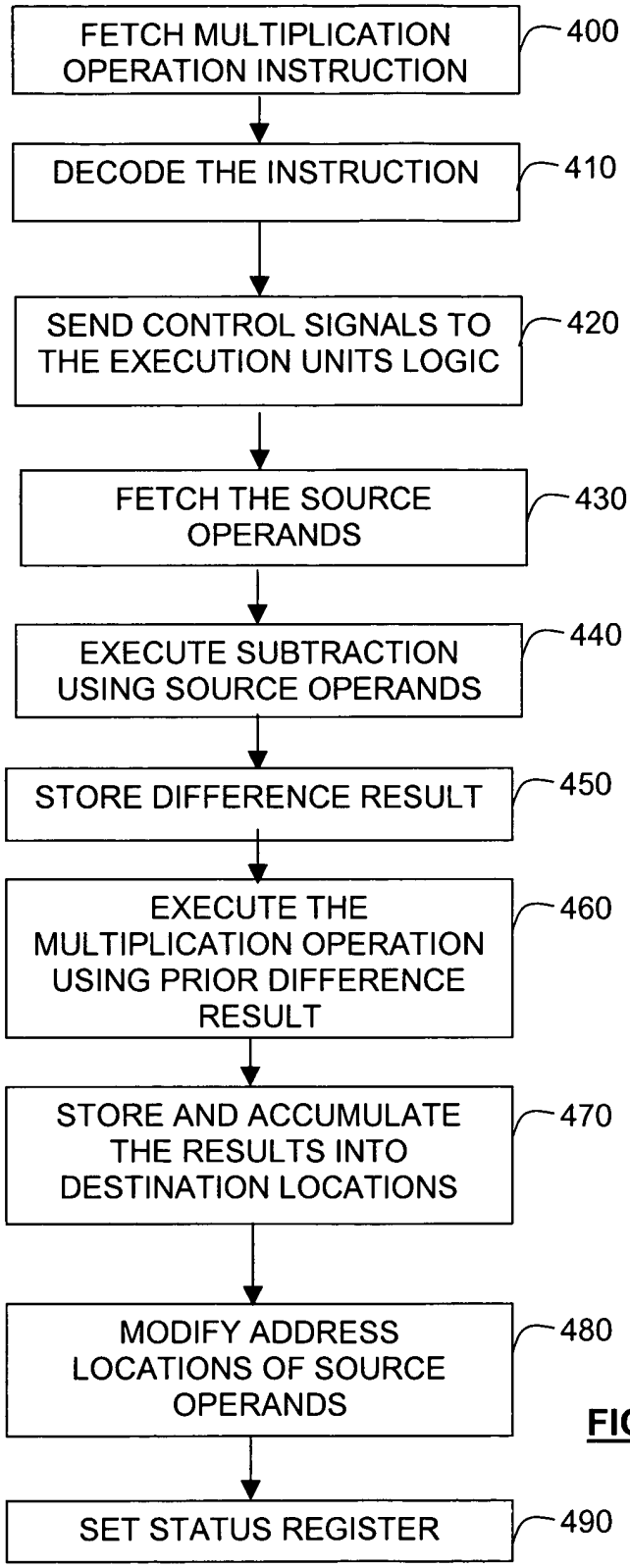

FIG. 4

Multiplication Operation Instructions

SYNTAX	ENCODING						DESCRIPTION
ED	1111	11mm	A1xx	00ii	iijj	jjii	computes $(A-B)^2$
EDAC	1111	11mm	A1xx	00ii	iijj	jjaa	computes $(A-B)^2 + C$

M bits- specifies register containing multiplication source operand
A bit- specifies destination location for square result
i bits- specifies register containing a difference operand, such as minuend
j bits- specifies register containing a difference operand, such as subtrahend
x bits- specifies destination location for difference result

FIG. 5